| L Number | Hits | | DB | Time stamp |
|----------|------|---|---------------------|---------------------|
| 7 | 2 | ("20030046492").PN. | USPAT; | 2004/10/28 16:25 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; IBM TDB | |
| 10 | 2562 | ((711/118,170,173).CCLS.) and @ad < | USPAT: | 2004/10/28 17:16 |
| 10 | 2502 | "20010828" | US-PGPUB; | 2001, 10, 20 1, 110 |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | · | IBM_TDB | |
| 11 | 37 | , , , , , | USPAT; | 2004/10/28 17:17 |
| | | "20010828") and (configur\$5 with memory with | US-PGPUB; | |
| | | cache)) and (configurable with memory) | EPO; JPO; | |
| | | | DERWENT; | |
| 12 | 90 | ((configurable near5 memory near5 cache) and | IBM_TDB USPAT; | 2004/10/28 17:17 |
| 12 | 90 | @ad <20010828) and (chip IC (integrated adj | US-PGPUB; | 2004/10/28 17:17 |
| | | circuit)) | EPO; JPO; | |
| | | 0110010,7 | DERWENT; | |
| | | | IBM TDB | |
| 13 | 43 | ((configur\$5 with memory) same (ram)) and | USPAT; | 2004/10/28 17:18 |
| | | (macro adj cell) and @ad < "20010828" | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| 14 | 80 | ((chip adj stack) and (integrated adj | IBM_TDB USPAT; | 2004/10/28 17:19 |
| 14 | 80 | circuit) and @ad < "20010828") and (flip adj | US-PGPUB; | 2004/10/28 17:19 |
| | | chip) | EPO; JPO; | |
| | | Chip, | DERWENT; | |
| | | | IBM TDB | |
| - | 2 | ("20030046492").PN. | USPAT; | 2004/04/28 09:05 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | 2000 | (711/110 170 170) 0010 | IBM_TDB | 2004/04/20 00:06 |
| _ | 2909 | (711/118,170,173).CCLS. | USPAT; US-PGPUB; | 2004/04/28 09:06 |
| | | | EPO; JPO; | |
| | | | DERWENT; | } |
| | | | IBM_TDB | |
| - | 336 | (((711/118,170,173).CCLS.) and @ad < | USPAT; | 2004/04/28 09:11 |
| | | "20010828") and (configur\$5 with memory with | US-PGPUB; | |
| | | cache) | EPO; JPO; | |
| | | | DERWENT; | |
| _ | 239 | configurable with memory with cache | IBM_TDB USPAT; | 2004/04/28 09:42 |
| | 233 | John Lagarabre wrett memory wrett eache | US-PGPUB; | 2003/04/20 09.42 |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| - | 150 | (configurable with memory with cache) and | USPAT; | 2004/04/28 09:41 |
| | - | @ad < "20010828" | US-PGPUB; | |
| | | | EPO; JPO; | |
| | · | | DERWENT; IBM TDB | |
| _ | 108 | ((configurable with memory with cache) and | USPAT; | 2004/04/28 09:42 |
| | | @ad < "20010828") and (chip IC (integrated | US-PGPUB; | |
| | | adj circuit)) | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| - | 104 | (configurable near5 memory near5 cache) and | USPAT; | 2004/04/28 09:48 |
| | | @ad <20010828 | US-PGPUB; | |
| | | | EPO; JPO; DERWENT; | |
| | | | IBM_TDB | |
| _ | 125 | ((configurable reconfigurable) near5 memory | USPAT; | 2004/04/28 09:48 |
| | | near5 cache) and @ad <20010828 | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |

| - | 89 | (((configurable reconfigurable) near5 memory near5 cache) and @ad <20010828) and (chip IC (integrated adj circuit)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/04/28 09:48 |
|---|-----|---|---|------------------|
| - | 17 | (US-6697916-\$ or US-6606686-\$ or US-6606684-\$ or US-6484237-\$ or US-6446181-\$ | IBM_TDB USPAT; US-PGPUB | 2004/04/28 15:53 |
| | | or US-6427190-\$ or US-6321318-\$ or US-5966734-\$ or US-6678790-\$ or US-6347346-\$ or US-6150839-\$).did. or (US-20020069331-\$ or US-20020078268-\$ or US-20020073282-\$ or US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$).did. | | |
| _ | o | ("118andfuse").PN. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/04/28 15:53 |
| - | 1 | ((US-6697916-\$ or US-6606686-\$ or US-6606684-\$ or US-6606684-\$ or US-6484237-\$ or US-6446181-\$ or US-6427190-\$ or US-6321318-\$ or US-5966734-\$ or US-6678790-\$ or US-6347346-\$ or US-6150839-\$).did. or (US-20020069331-\$ or US-20020078268-\$ or US-20020073282-\$ or US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$).did.) and fuse | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/04/28 15:54 |
| - | | ((US-6697916-\$ or US-6606686-\$ or US-6606684-\$ or US-6606684-\$ or US-6446181-\$ or US-6427190-\$ or US-6321318-\$ or US-5966734-\$ or US-6678790-\$ or US-6347346-\$ or US-6150839-\$).did. or (US-20020069331-\$ or US-20020078268-\$ or US-20020073282-\$ or US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$).did.) and burn | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/04/28 15:58 |
| - | 608 | | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/04/28 15:59 |
| _ | 350 | ((memory with configur\$5) with fuse) and @ad < "20010828" | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/04/28 15:59 |
| - | 78 | (((memory with configur\$5) with fuse) and @ad < "20010828") and processor | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/04/29 13:52 |
| - | 1 | ("0591539").PN. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB | 2004/04/29 09:19 |
| - | o | ("9591539").PN. | USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/04/29 09:19 |
| - | 0 | "09591539" | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/04/29 09:20 |
| - | 0 | "09591537" | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; | 2004/04/29 09:20 |
| L | L | | IBM_TDB | |

| - | 33 | "09/591537" | USPAT; | 2004/04/29 09:20 |
|-----|---------|--|------------------------|------------------|
| | | | US-PGPUB; EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| - | 9656 | | USPAT; | 2004/04/29 14:05 |
| | | (integrated adj circuit) | US-PGPUB; | |
| | | | EPO; JPO; DERWENT; | |
| | | | IBM TDB | |
| _ | 389 | (chip adj stack) and (integrated adj | USPAT; | 2004/04/29 14:12 |
| | | circuit) and @ad < "20010828" | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; IBM TDB | |
| _ | 34 | ((chip adj stack) and (integrated adj | USPAT; | 2004/04/29 13:55 |
| | 1 | circuit) and @ad < "20010828") and fuse | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| _ | 6794 | (flip adj chip) and (integrated adj | IBM_TDB USPAT; | 2004/04/29 14:06 |
| | 0,51 | circuit) and @ad < "20010828" | US-PGPUB; | |
| | | | EPO; JPO; | |
| + | 1 | | DERWENT; | |
| _ | 126 | ((flip adj chip) and (integrated adj | IBM_TDB USPAT; | 2004/04/29 14:08 |
| | 420 | circuit) and @ad < "20010828") and fuse | US-PGPUB; | 2004/04/23 14.00 |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | 1464560 | // /57 to and about the countries and | IBM_TDB | 2004/04/20 24 00 |
| - | 1464760 | (((flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse) | USPAT; US-PGPUB; | 2004/04/29 14:08 |
| | | memory | EPO; JPO; | |
| | | | DERWENT; | |
| | | (, , , , , , , , , , , , , , , , , , , | IBM_TDB | |
| - | 199 | (((flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse) and | USPAT; | 2004/04/29 14:11 |
| | | memory | US-PGPUB; EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| j - | 0 | ((((flip adj chip) and (integrated adj | USPAT; | 2004/04/29 14:11 |
| | | circuit) and @ad < "20010828") and fuse) and memory) and (chip adj stack) | US-PGPUB; EPO; JPO; | |
| | | memory, and temp and beach, | DERWENT; | |
| | | | IBM_TDB | |
| - | 21 | (US-6697916-\$ or US-6606686-\$ or | USPAT; | 2004/04/29 15:12 |
| | | US-6606684-\$ or US-6484237-\$ or US-6446181-\$ or US-6427190-\$ or US-6321318-\$ or | US-PGPUB | |
| | | US-5966734-\$ or US-6678790-\$ or US-6347346-\$ | | |
| | | or US-6150839-\$ or US-6355968-\$ or | | |
| | | US-6297666-\$).did. or (US-20020069331-\$ or | | |
| | | US-20020078268-\$ or US-20020073282-\$ or US-20020069341-\$ or US-20020065867-\$ or | | |
| - | | US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$ or US-20010049174-\$ or | | |
| | | US-20020074668-\$).did. | | |
| - | 1 | ((US-6697916-\$ or US-6606686-\$ or | USPAT; | 2004/04/29 15:14 |
| | | US-6606684-\$ or US-6484237-\$ or US-6446181-\$ | US-PGPUB; | |
| | | or US-6427190-\$ or US-6321318-\$ or US-5966734-\$ or US-6678790-\$ or US-6347346-\$ | EPO; JPO; DERWENT; | |
| | | or US-6150839-\$ or US-6355968-\$ or | IBM TDB | |
| | | US-6297666-\$).did. or (US-20020069331-\$ or | _ | |
| | | US-20020078268-\$ or US-20020073282-\$ or | | |
| | | US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$ or US-20010049174-\$ or | | |
| | | US-20020074668-\$).did.) and ((macro adj | | |
| | | cell) macrocell) | | |
| - | 0 | (configurable with memory) same (ram and | USPAT; | 2004/04/29 15:16 |
| | | cache) same (macro adj cell) and @ad < "20010828" | US-PGPUB; | |
| | | 20010020 | EPO; JPO; DERWENT; | |
| | | | IBM TDB | |

| - | 0 | (configurable with memory) same (ram) same | USPAT; | 2004/04/29 15:16 |
|---|-----|--|-----------|------------------|
| | | (macro adj cell) and @ad < "20010828" | US-PGPUB; | Í |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | (/ | IBM_TDB | 2004/04/20 15 16 |
| _ | 0 | 1 ,, | USPAT; | 2004/04/29 15:16 |
| | | cache)) and (macro adj cell) and @ad < | US-PGPUB; | Ì |
| | | "20010828" | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| - | 9 | (| USPAT; | 2004/04/29 15:25 |
| | | (macro adj cell) and @ad < "20010828" | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |
| - | 246 | , | USPAT; | 2004/04/29 15:26 |
| | | @ad < "20010828" | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | DERWENT; | |
| | | | IBM_TDB | |